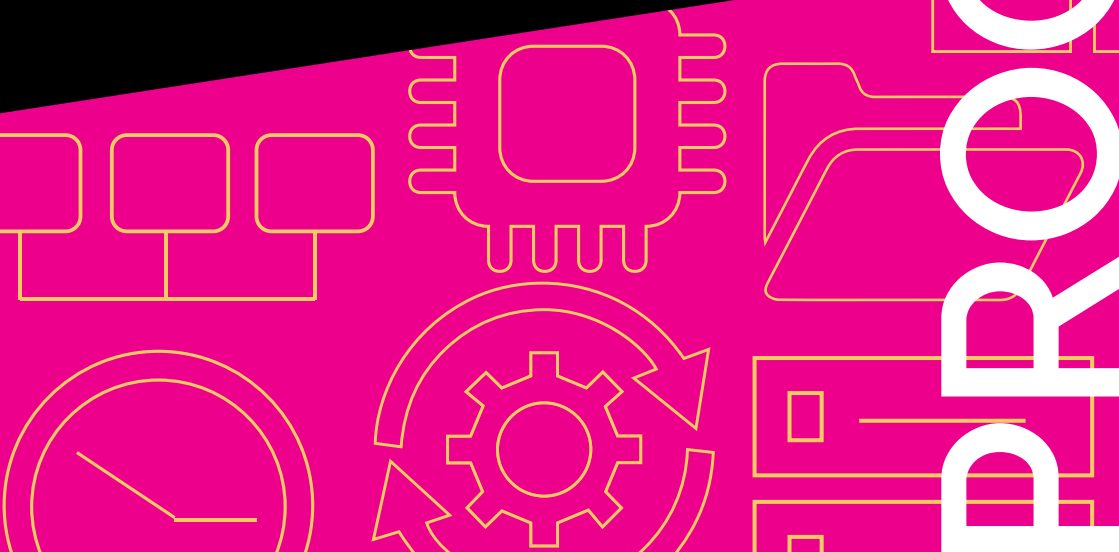


<EURO/SYS'22>



A premier forum for

SYSTEMS SOFTWARE RESEARCH



EURO/SYS'22
PROGRAM

to Gilles Muller



April 22

Dear EuroSys Friends

It is our pleasure to welcome you to EuroSys 2022, the 17th edition of the European Conference on Computer Systems! After two years of online presence due to the COVID-19 pandemic, EuroSys is a physical conference again, hosted in Rennes, the historical capital of Brittany in France.

Main Conference Program

This year's program features exciting developments along key aspects of systems research. The accepted papers present the latest advances in systems for machine learning, distributed systems, security, concurrency and failure tolerance, storage systems, operating systems, and cloud infrastructure.

We received 161 submissions that were reviewed in two rounds by a program committee of 35 heavy and 59 light PC members (chairs excluded). Each paper received 3 to 8 reviews and the total number of reviews was 765. Similar to EuroSys'21 and most other systems conferences, we allowed authors to

respond to the reviewers' questions and address any misconceptions. After a vibrant online discussion and a daylong virtual PC meeting, we decided to accept 42 papers and solicit revised manuscripts for 4 more submissions. Three of these papers met the revision requirements set by the PC and were included in the final conference program. Overall, we accepted 45 papers for an acceptance rate of 27.6%. The high acceptance rate reflects the vitality of our growing research community across the world.

A significant fraction of our community is still unable to travel to conferences due to the COVID-19 related restrictions. Hence, we decided that

the conference presentations will follow a hybrid format, allowing for both remote presenters and remote conference participants. We hope that the hybrid format will allow a broader audience to join the conference.

Artifact Evaluation (AE)

EuroSys'22 included an Artifact Evaluation (AE) process, organized by Anjo Vahldiek Oberwagner (Intel Labs), Salvatore Signorello (University of Lisbon), Natacha Crooks (UC Berkeley), and Solal Pirelli (EPFL). The AE committee included 65 volunteers. The authors of accepted papers were given the opportunity to submit an artifact for their research. The artifact could include source code, systems, configuration, datasets, models, test suites, benchmarks, and any other material underlying the paper's contributions. This year, the AEC received a total of 32 artifact submissions and each submission was independently evaluated by 3 or 4 AEC members. All 32 submissions received at least one AE badge. Overall, the AEC awarded 32 Artifacts Available badges (100% acceptance rate), 27 Artifacts Functional badges (96% acceptance rate), and 20 Results Reproduced badges (77% acceptance rate).

Shadow PC

In parallel to the main PC, we also organized a Shadow PC. The Shadow PC was chaired by Bisseyandé Tégawendé (University of Luxembourg) and Gaël Thomas (Telecom SudParis). The Shadow PC is an integral part of the EuroSys community-building exercise: serving on a shadow PC is an excellent opportunity for young systems researchers (PhD students,

postdocs, and new faculty members) to gain experience in program committee practices. The 2022 Shadow PC included 79 PC members 62 were PhD students, 15 early career researchers in academia and 2 early career researchers in industry. None of them have served on a program committee before. As part of this process, 128 papers were reviewed by the Shadow PC in one round of reviews, followed by a 5-day online discussion stage and daylong virtual PC meeting. The Shadow PC has recommended an Accept decision for 20 of the papers and a Revise decision to 15 more.

Workshops

In addition to the main program, EuroSys'22 features seven workshops, coordinated by the workshop chairs Erwan Le Merrer (Inria, Rennes) and Renaud Lachaize (University of Grenoble). The workshop list includes well-established workshops such as the 16th EuroSys Doctoral Workshop (EuroDW), the 15th European Workshop on Systems Security (EUROSEC), the 11th Workshop on Systems for Post-Moore Architectures (SPMA), the 9th Workshop on Principles and Practice of Consistency for Distributed Data (PaPoC), and the 5th International Workshop on Edge Systems, Analytics and Networking (EdgeSys). It also features newer workshops on emerging issues such as the 2nd European Workshop on Machine Learning and Systems (EuroMLSys) and the 2nd Workshop on Challenges and Opportunities of Efficient and Performant Storage Systems (CHEOPS).

The EuroDW doctoral workshop is a

particularly important forum at EuroSys, aiming to inspire young systems researchers. This year's EuroSys doctoral workshop is chaired by Oana Balmau (McGill University), Valerio Schiavoni (University of Neuchâtel), and Pierre Sutra (Télécom SudParis). The 16th EuroDW will provide a forum for PhD students to present their work and receive constructive feedback from experts in the field as well as from their peers. Technical presentations will be augmented with general advice and discussions about getting a PhD, doing research, and career perspectives. PhD students at any stage of their doctoral studies may participate in the workshop.

Acknowledgements

Lastly, we are thankful to the many people that helped organize EuroSys'22 during another challenging year for our community. The core organization team includes: the financial chair Guillaume Pierre (University of Rennes 1); the publicity chairs Sonia Ben Mokhtar (CNRS, Lyon) and Kahina Lazri (Orange); the sponsorship chairs Heming Cui (HKU), Daniel Berger (Microsoft Research), Laurent Réveillère (University of Bordeaux), and Alain Tchana (ENS, Lyon); the publications chair Julia Lawall (Inria, Paris); the website chairs François Taïani (University of Rennes 1) and Renaud Lachaize (University of Grenoble); the grant chair Pierre Olivier (University of Manchester); the poster chairs Mvondo Djob (Univ Rennes, Inria, CNRS, IRISA) and Romain Rouvoy (University of Lille). The submission chairs, Kostis Kaffes (Stanford University) and Rafael Pires (EPFL) were essential in running all aspects of the program review process

as well as Rishi Sharma (EPFL) who helped us run the online PC meeting. Finally, we want to thank the EuroSys Steering Committee for the continued support throughout this process.

We also want to acknowledge the generous support by the EuroSys'22 sponsors: Huawei and Stormshield (platinum sponsors); Microsoft, Protocol Labs Research, Red Hat, and Amazon (gold sponsors); Meta (silver sponsor); Google, Intel, and Cisco (bronze sponsors).

Dedication

EuroSys'22 is dedicated to our dearly departed colleague, Gilles Muller. Our entire community was extremely saddened by his passing. Beyond being a brilliant and impactful systems researcher, Gilles was instrumental in developing and promoting European systems research. His deep involvement in EuroSys was as enthusiastic as effective and his work has been an inspiration to all of us. Gilles also spent a significant part of his career in Rennes and would have loved to attend this EuroSys edition. He will be missed by all of us. EuroSys'22 includes a special workshop to honor his memory.

We hope you enjoy the conference, in Rennes or online.

Anne-Marie Kermarrec (EPFL),
Program Co-chair
Christos Kozyrakis (Stanford University),
Program Co-chair
David Bromberg (University of Rennes 1),
General Chair

Espace Conférences
Inria Rennes

1:30 AM - 5:00 PM

MONDAY

Remembering Gilles Muller [1962 - 2021]

Introduction	13:30
Technical Talks	14:00
Departure for dinner	17:15

Gilles Muller began his studies in math/physics at Paris 6 in 1980. Based on happy childhood memories of sailing in Brittany and a fervent desire to escape punched cards, in 1982 he transferred to INSA Rennes, which was specialized in computer science and had the most up to date hardware at the time. Having acquired a deep love of research, as well as a substantial expertise in the Rubik's cube, in 1985 he started a PhD at IRISA in the LSP team, under the direction of Jean-Pierre and Michel Banatre. Starting from his PhD involving designing a stable memory, through the rest of his career at Inria Rennes, the Ecole de Mines de Nantes, and Inria Paris, the guiding theme of Gilles Muller's career was the goal of reliable systems software. In the Compose team at Rennes, with Charles Consel, in 1996-2002, he explored the use of domain-specific languages, making it possible to express system policies in a high-level way that both eased the task of the programmer and made the code more amenable to ad hoc verifications. He explored a number of systems domains, including the development of device drivers, with the Devil language published at OSDI in 2000. Starting in 2004, in the Obasco team at Nantes, he led the development of another kind of DSL targeting systems challenges, Coccinelle for matching and transforming device driver code. This work was published at EuroSys

2006 and 2008, and is still used today by Linux kernel developers. In 2009, Gilles Muller joined Inria Paris. In 2014, he formed his own team, Whisper, and plunged into the formal proof of scheduling code, still leveraging a domain-specific language approach. This work, on Ipanema, was published in EuroSys 2020. His dream remained push-button verification of systems code; a step in this direction was the VeriAmos ANR project starting in 2019 led by Xavier Rival. At the same time, as is inevitable in systems, he was concerned with performance, as illustrated by his work on remote core locking, during the PhD of Jean-Pierre Lozi, completed in 2014, and the Nest scheduling policy, which will be presented at EuroSys 2022. Some of these topics will be presented in this workshop. Gilles Muller was also a tireless promoter of the systems community in France and in Europe as a whole. The organizers and speakers would like to thank him for the many opportunities he has created, through his contributions to the ASF, Compas, EuroSys, and ACM SIGOPS, through the ad hoc workshops he organized, and through informal discussions






< Workshops >

<EURO/SYS'22>

Couvent des Jacobins

8:00 AM - 18:30 PM

TUESDAY

08:00	Registration	
08:30	Workshops starts	
10:00	Coffee break	
12:30	Workshops ends	
12:30	Lunch break	
13:45	Workshops starts	
16:00	Coffee break	
18:30	Workshops ends	

Systems for Post-Moore Architectures SPMA



08:30 12:30

Principles and Practice of Consistency for Distributed Data PaPoC



08:30 18:30

International Workshop on Edge Systems EdgeSys



09:00 17:50

European Workshop on Systems Security EuroSec



08:45 18:30

European Workshop on Machine Learning and Systems EuroMLSys



09:00 18:15

Challenges and Opportunities of Efficient and Performant Storage Systems CHEOPS



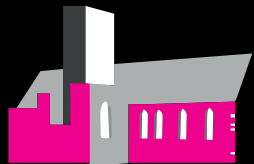
13:40 18:30

Eurosys Doctoral Workshop 2022 EuroDW



09:00 18:00




CONFERENCE



Couvent des Jacobins




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WEDNESDAY
APRIL

08:30	Registration
09:00	Opening remarks
09:30	BFT
10:30	Coffee break 
11:00	Concurrency
12:20	Lunch 
13:30	Software security
15:10	Coffee break 
15:40	ML for Systems
17:00	Poster session
19:00	End of day one



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THURSDAY
APRIL

08:30	Registration
09:00	Trusted Execution
10:20	Coffee break 
10:50	Edge, Embedded
	Operating Systems
12:30	Lunch 
14:00 (small)	Systems for ML
	Systems for ML
(large)	
15:40	Coffee break 
16:10	Persistent Memory
	Big Data
17:30	Short break
17:45	General Assembly
18:45	Cocktail and Dinner

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FRIDAY
APRIL

08:30	Registration
09:00	SSDs & I/O
10:20	Coffee Break 
10:50	FaaS 1
12:10	Lunch 
13:30	FaaS 2
	Misc
14:30	Closing session
15:00	End of conference

<EURO/SYS'22>

Conference

Couvent des Jacobins

8:30 AM - 19:00 PM

WEDNESDAY

08:30 Registration

09:00 Opening remarks

09:30 **BFT**

Session Chair: Rodrigo Rodrigues

DAMYSUS: Streamlined BFT Consensus Leveraging Trusted Components

Jeremie Decouchant (Delft University of Technology), David Kozhaya (ABB Corporate Research), Vincent Rahli (University of Birmingham), Jiangshan Yu (Monash University)

State Machine Replication Scalability Made Simple

Chrysoula Stathakopoulou (IBM Research, Zurich, ETH Zurich), Matej Pavlovic (IBM Research, Zurich), Marko Vukolic (Protocol Labs)

Narwhal and Tusk: A DAG-based Mempool and Efficient BFT Consensus

George Danezis (MystenLabs / University College London), Lefteris Kokoris-Kogias (IST Austria), Alberto Sonnino (MystenLabs), Alexander Spiegelman (Novi Research)

10:30 Coffee break



11:00 **Concurrency**

Session Chair: Jean-Pierre Lozi

Building an Efficient Key-Value Store in a Flexible Address Space

Chen Chen and Wenshao Zhong (University of Illinois at Chicago), Xingbo Wu (Microsoft Research)

Rolis: a software approach to efficiently replicating multi-core transactions

Weiwei Shen and Ansh Khanna (Stony Brook University), Sebastian Angel (University of Pennsylvania and Microsoft Research), Siddhartha Sen (Microsoft Research), Shuai Mu (Stony Brook University)

Tebis: Index Shipping for Efficient Replication in LSM Key-Value Stores

Michalis Vardoulakis (Univ. of Crete and FORTH, Greece), Giorgos Saloustros (FORTH), Pilar González-Férez (University of Murcia, Spain), Angelos Bilas (Univ. of Crete and FORTH, Greece)

12:20 Lunch



13:30 **Software security**

Session Chair: Angelos Bilas

Sharing is Caring: Secure and Efficient Shared Memory Support for MVEEs

Jonas Vinck (imec-DistriNet, KU Leuven), Bert Abrath and Bart Coppens (Ghent University), Alexios Voulimeneas (imec-DistriNet, KU Leuven), Bjorn De Sutter (Ghent University), Stijn Volckaert (imec-DistriNet, KU Leuven)

Hardening Binaries against More Memory Errors

Gregory J. Duck, Yuntong Zhang, and Roland H.C. Yap (National University of Singapore)

PKRU-Safe: Automatically Locking Down the Heap Between Safe and Unsafe Languages

Paul Kirth (University of California, Irvine), Mitchel Dickerson (University of California, Irvine), Stephen Crane and Per Larsen (Immunant, Inc.), Adrian Dabrowski, David Gens, and Yeoul Na (University of California, Irvine), Stijn Volckaert (imec-DistriNet, KU Leuven), Michael Franz (University of California, Irvine, USA)

KASLR in the age of MicroVMs

Benjamin Holmes and Jason Waterman (Vassar College), Dan Williams (Virginia Tech)

<EURO/SYS'22>

Conference

Couvent des Jacobins

8:30 AM - 19:00 PM

WEDNESDAY

Nyx-Net: Network Fuzzing with Incremental Snapshots

Sergej Schumilo and Cornelius Aschermann (Ruhr University Bochum), Andrea Jemmett (Vrije Universiteit Amsterdam), Ali Abbasi (Ruhr University Bochum), Thorsten Holz (CISPA Helmholtz Center for Information Security)

15:10

Coffee break



15:40 ML for Systems

Session Chair: Zsolt István

DeepRest: Deep Resource Estimation for Interactive Microservices

Ka-Ho Chow (Georgia Institute of Technology), Umesh Deshpande and Sangeetha Seshadri (IBM Research – Almaden), Ling Liu (Georgia Institute of Technology)

Unicorn: Reasoning about Configurable System Performance through the lens of Causality

Md Shahriar Iqbal (University of South Carolina), Rahul Krishna (Columbia University), Mohammad Ali Javidian (Purdue University), Baishakhi Ray (Columbia University), Pooyan Jamshidi (University of South Carolina)

Multi-Objective Congestion Control

Yiqing Ma, Han Tian, Xudong Liao, Junxue Zhang, Weiyan Wang, and Kai Chen (Hong Kong University of Science and Technology), Xin Jin (Peking University)

Hybrid Anomaly Detection and Prioritization for Network Logs at Cloud Scale

David Ohana, Bruno Wassermann, Nicolas Dupuis, Elliot Kolodner, Eran Raichstein, and Michal Malka (IBM Research)

17:00 Poster session & Breizh Cocktail



19:00

End of day one

<EURO/SYS'22>

Conference

08:30 Registration

09:00 Trusted Execution

Session Chair: Valerio Schiavoni

Performance Evolution of Mitigating Transient Execution Attacks

Jonathan Behrens, Adam Belay, and M. Frans Kaashoek (MIT CSAIL)

You Shall Not (by)Pass! Practical, Secure, and Fast PKU-based Sandboxing

Alexios Voulimeneas, Jonas Vinck, Ruben Mechelinck, and Stijn Volckaert (imec-DistriNet, KU Leuven)

Verified Programs Can Party: Optimizing Kernel Extensions via Post-Verification In-Kernel Merging

Hsuan-Chi Kuo, Kai-Hsun Chen, and Yicheng Lu (University of Illinois at Urbana-Champaign), Dan Williams (Virginia Tech), Sibin Mohan (Oregon State University), Tianyin Xu (University of Illinois at Urbana-Champaign)

Minimum Viable Device Drivers for ARM TrustZone

Liwei Guo and Felix Xiaozhu Lin (University of Virginia)

10:20 Coffee break



Couvent des Jacobins

8:30 AM - 23:00 PM

THURSDAY

10:50 Edge, Embedded

Session Chair: Eiko Yoneki

OPEC: Operation-based Security Isolation for Bare-metal Embedded Systems

Xia Zhou, Jiaqi Li, Wenlong Zhang, and Yajin Zhou (Zhejiang University), Wenbo Shen (Zhejiang University), Kui Ren (Zhejiang University)

LiteReconfig: Cost and Content Aware Reconfiguration of Video Object Detection Systems for Mobile GPUs

Ran Xu, Jayoung Lee, Pengcheng Wang, and Saurabh Bagchi (Purdue University), Yin Li (University of Wisconsin - Madison), Somali Chaterji (Purdue University)

Operating Systems

Session Chair: Gaël Thomas

Slashing the Disaggregation Tax in Heterogeneous Data Centers with FractOS

Lluís Vilanova (Imperial College London), Lina Maudlej and Shai Bergman (Technion), Till Miemietz (Barkhausen Institut), Matthias Hille (TU Dresden), Nils Asmussen and Michael Roitzsch (Barkhausen Institut), Hermann Härtig (TU Dresden), Mark Silberstein (Technion)

OS Scheduling with Nest: Keeping Tasks Close Together

on Warm Cores

Julia Lawall and Himadri Chhaya-Shailesh (Inria), Jean-Pierre Lozi (Oracle Labs), Baptiste Lepers and Willy Zwaenepoel (University of Sydney), Gilles Muller (Inria)

Kite: Lightweight Critical Service Domains

A K M Fazla Mehrab (Virginia Tech), Ruslan Nikolaev (The Pennsylvania State University), Binoy Ravindran (Virginia Tech)

12:30 Lunch



14:00 Systems for ML (small)

Session Chair: Baptiste Lepers

Fleche: An Efficient GPU Embedding Cache for Personalized Recommendations

Minhui Xie, Youyou Lu, Jiazhen Lin, Qing Wang, and Jian Gao (Tsinghua University), Kai Ren (Kuaishou Technology), Jiwu Shu (Tsinghua University)

GNNLab: A Factored System for Sample-based GNN Training over GPUs

Jianbang Yang (IPADS, Shanghai Jiao Tong University), Dahai Tang (Hunan University), Xiaoni Song (IPADS, Shanghai Jiao Tong University, Shanghai AI Laboratory), Lei Wang (Alibaba)

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Conference

Group), Qiang Yin (BASICS, Shanghai Jiao Tong University), Rong Chen (IPADS, Shanghai Jiao Tong University, Shanghai AI Laboratory), Wenyuan Yu and Jingren Zhou (Alibaba Group)

Out-Of-Order BackProp: An Effective Scheduling Technique for Deep Learning

Hyungjun Oh, Junyeol Lee, Hyeongju Kim, and Jiwon Seo (Hanyang University)

Systems for ML (large)

Session Chair: Marco Canini

D3: A Dynamic Deadline-Driven Approach for Building Autonomous Vehicles

Ionel Gog, Sukrit Kalra, Peter Schafhalter, Joseph Gonzalez, and Ion Stoica (UC Berkeley)

Varuna: Scalable, Low-cost Training of Massive Deep Learning Models

Sanjith Athlur (CMU), Nitika Saran (Cornell), Muthian Sivathanu, Ramachandran Ramjee, and Nipun Kwatra (Microsoft Research India)

15:40

Coffee break



16:10 Persistent Memory

Session Chair: Stijn Volckaert

Characterizing the Performance of Intel Optane Persistent Memory — A Close Look at its On-DIMM Buffering

Lingfeng Xiang (The University of Texas at Arlington), Xingsheng Zhao (University of Texas at Arlington), Jia Rao (The University of Texas at Arlington), Song Jiang (University of Texas at Arlington), Hong Jiang (UT Arlington)

SafePM: A Sanitizer for Persistent Memory

Kartal Kaan Bozdoğan (Technical University of Munich), Dimitrios Stavrakakis (Technical University of Munich & University of Edinburgh), Shady Issa and Pramod Bhatotia (Technical University of Munich)

ResPCT: Fast Checkpointing in Non-Volatile Memory for Multi-Threaded Applications

Ana Khorguani, Thomas Ropars, and Noel De Palma (Univ. Grenoble Alpes, CNRS, Grenoble INP, LIG)

Big Data

Session Chair: Etienne Rivière

Optimizing the Interval-centric Distributed Computing Model for Temporal Graph Algorithms

Animesh Baranawal and Yogesh Simmhan (IISc, Bangalore)

A New Benchmark Harness for Systematic and Robust Evaluation of Streaming State Stores

Esmail Asyabi, Yuanli Wang, John Liagouris, Vasiliki Kalavri, and Azer Bestavros (Boston University)

17:30

Short break

17:45

General Assembly



Couvent des Jacobins

8:30 AM - 23:00 PM

THURSDAY

<EURO/SYS'22>

Conference

08:30 Registration

09:00 SSDs & I/O

Session Chair: Julia Lawal

p2KVS: a Portable 2-Dimensional Parallelizing Framework to Improve Scalability of Key-value Stores on SSDs

Ziyi Lu and Qiang Cao (Huazhong University of Science and Technology), Hong Jiang (University of Texas at Arlington), Shucheng Wang (Huazhong University of Science and Technology), Yuanyuan Dong (Alibaba Group)

Improving Scalability of Database Systems by Reshaping User Parallel I/O

Ning Li (Dept. of Computer Science and Engineering, University of Texas at Arlington, USA), Hong Jiang (UT Arlington), Hao Che (Department of Computer Science and Engineering, The University of Texas at Arlington), Zhijun Wang (Dept. of Computer Science and Engineering, University of Texas at Arlington, USA), Minh Q. Nguyen (Faculty of IT, Ho Chi Minh City University of Transport)

BetrFS: A Compleat File System for Commodity SSDs

Yizheng Jiao (The University of North Carolina at Chapel Hill), Simon Bertron (Katana Graph), Sagar Patel and Luke Zeller (The University of North Carolina at Chapel Hill), Rory Bennett (Stony Brook University), Nirjhar Mukherjee (Carnegie Mellon University), Michael Bender (Stony Brook University), Michael Conduct (unaffiliated), Alex Conway (Vmware Research), Martin Farach-Colton (Rutgers University),

XIONGZI GE (NetApp Inc.), William Jannen (Williams College), Rob Johnson (VMWare Research), Donald Porter (The University of North Carolina at Chapel Hill), Jun Yuan (Pace University)

Beating the I/O bottleneck: A case for log-structured virtual disks

Mohammad Hossein Hajkazemi (NetApp), Vojtech Aschenbrenner (EPFL), Mania Abdi (Northeastern University), Emine Ugur Kaynar, Amin Mossayebzadeh, Orran Krieger (Boston University), Peter Desnoyers (Northeastern University)

10:20 Coffee Break



10:50 FaaS 1

Session Chair: Lluís Vilanova

Isolating Functions at the Hardware Limit with Virtines

Nicholas Wanninger (Northwestern University), Joshua Bowden, Kirtankumar Shetty, Ayush Garg, and Kyle Hale (Illinois Institute of Technology)

Fireworks: A Fast, Efficient, and Safe Serverless Framework using VM-level post-JIT Snapshot

Wonseok Shin (SK Telecom), Wook-Hee Kim (Konkuk University), Changwoo Min (Virginia Tech)

VMSH: Hypervisor-agnostic Guest Overlays for VMs

Jörg Thalheim (TU Munich / University of Edinburgh), Peter Okelmann, Harshavardhan Unnibhavi, Redha Gouicem, and Pramod Bhatotia (TU Munich)

Jiffy: Elastic Far-Memory for Stateful Serverless Analytics

Anurag Khandelwal and Yupeng Tang (Yale University), Rachit Agarwal (Cornell University), Aditya Akella (UT Austin), Ion Stoica (UC Berkeley)

12:10 Lunch



13:30 FaaS 2

Session Chair: Anne-Marie Kermarrec

Memory Deduplication for Serverless with Medes

Divyanshu Saxena, Tao Ji (UT Austin), Arjun Singhvi, Junaid Khalid (UW-Madison), Aditya Akella (UT Austin)

FaaSnap: FaaS Made Fast Using Snapshot-based VMs

Lixiang Ao, George Porter, Geoffrey M. Voelker (UC San Diego)

Misc

APT-GET: Profile-Guided Timely Software Prefetching

Saba Jamilan (University of California, Santa Cruz), Tanvir Ahmed Khan (University of Michigan), Grant Ayers (Google), Baris Kasikci (University of Michigan), Heiner Litz (University of California, Santa Cruz)

14:30 Closing session

15:00 End of conference

Couvent des Jacobins

8:30 AM - 15:00 PM

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Pierre Sutra, *Télécom SudParis, France*

& Thanks to Virginie Desroches, Elisabeth Lebrete and Agnès Cottais for their precious involvement.

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Marc Shapiro, *INRIA*

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Adriana Szeekeres, *Univ. of Washington*

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Roger Watterhofer, *ETHZ*

Hakim Weathspoon, *Cornell University*

Yubin Xia, *Shanghai Jiao Tong University*

Wei Xu, *Tsinghua University*

Neeradja Yadwadkar, *VMware Research*

Eiko Yoneki, *Cambridge*

Ce Zhang, *ETH*

Qing Zheng, *CMU*

Location

THE SPECIAL EVENT "REMEMBERING GILLES MULLER"



<EURO/SYS'22> Conference & Workshops

Espace Conférences
Inria Rennes

1:30 AM - 5:00 PM

MONDAY

Inria

Rennes Bretagne-Atlantique



Campus de Beaulieu
263 Av. du Général Leclerc
(Allée Jean Perrin), Rennes

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Place Sainte Anne

4

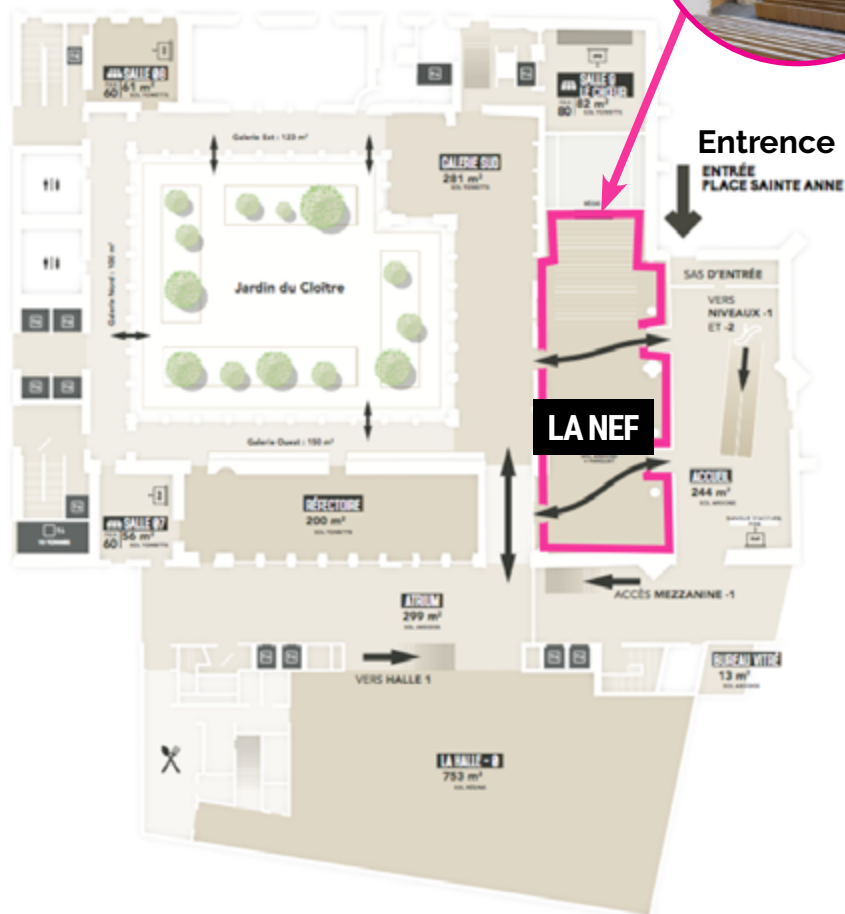
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Location

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RDC / LEVEL 0

Conference room

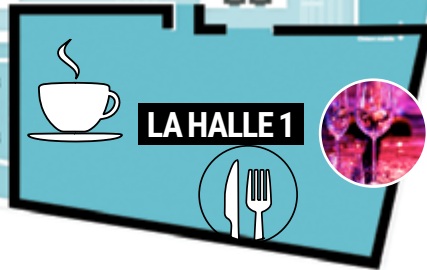


1^{er} ÉTAGE / LEVEL 1

Workshops rooms



Break, Lunch &
Gala dinner room



2^e ÉTAGE / LEVEL 2

Workshops rooms



CONTACTS

For any question(s) related to EuroSys 2022, please contact us:

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General enquiries



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